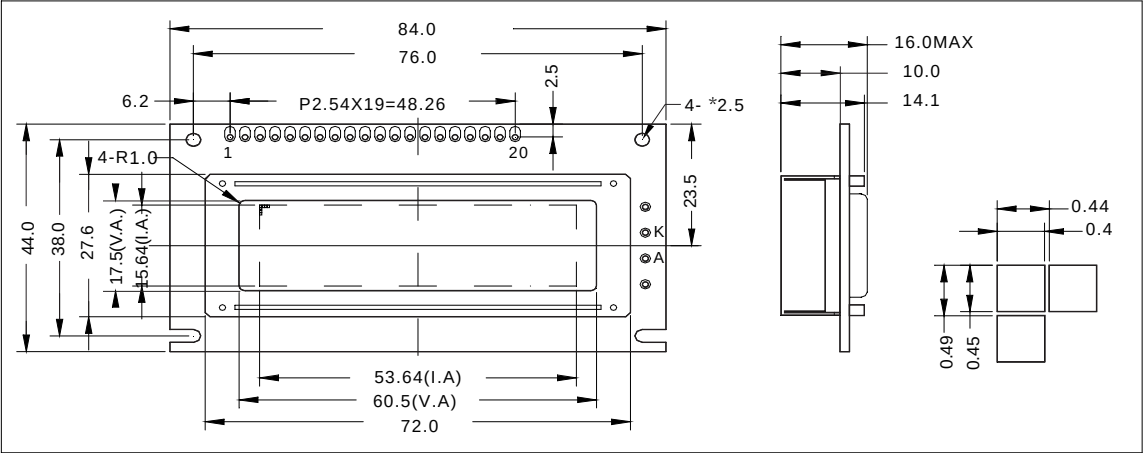


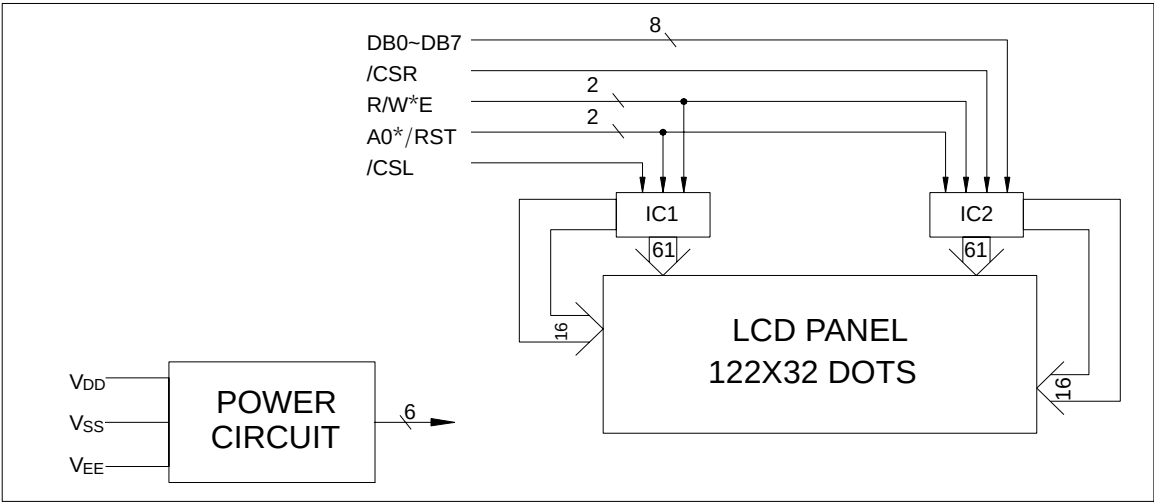
1.0 Features

- * Display Mode: Reflective/Transflective/Transmissive and Positive Type STN
- * Input Data: 8-Bits Parallel Data Input from a MPU
- * Assembly: SMT / COB
- * Backlight: Optional

2.0 External Dimension



3.0 Block Diagram



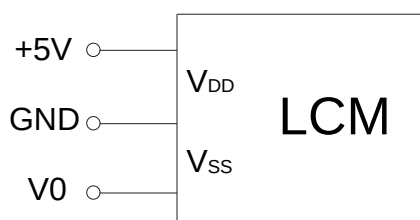
4.0 Maximum Rating

Item	Symbol	Test Condition	Standard Value		Unit
			Min.	Max.	
Supply Voltage for Logic	V _{DD} –V _{SS}	Ta=25 °C	0	7.0	V
Supply Voltage for LCD	V _{DD} –V _{EE}		0	10.0	V
Input Voltage	V _I		0	V _{dd}	V
Operating Temperature	T _{opr}	—	0	+50	°C
Storage Temperature	T _{stg}	—	–20	+70	°C

5.0 Electro-Optical Characteristics

Item		Symbol	Conditions	Standard Value			Unit
				Min.	Typ.	Max.	
Power Supply for Logic	Logic	Vdd	—	4.5	5.0	5.5	V
	LCD Drive	Vdd-Vee		—	5.0	—	
Frame Frequency		f FLM	Vdd=5.0V	60	65	70	Hz
Current Consumption		Idd	Vdd=5.0V, Vdd-V0=5.0 f FLM=75Hz	—	—	2.0	mA
LCD Driving Voltage(Recommended)		Vdd-V0	Ta=25 °C; Ø, θ=0°	—	5.0	—	V
Response Time(Rising)		Tr	Ta=25 °C; Ø, θ=0°	—	200	250	ms
Response Time(Decay)		Td		—	250	300	ms
Viewing Angle		Ø2-Ø1	K≥2	-10	—	45	DEG.
Contrast Ratio		K	Ø=0° , θ=0°	2.0	5.0	—	—

6.0 Power Supply for LCM



7.0 I/O Connection

Pin No	Symbol	Level	Function
1	VSS	—	Ground
2	VDD	—	Power supply for logical circuit (5V).
3	V0	—	Power supply for LCD driving.
4	A0	H/L	MPU inputs transfer data/instruction selection signal.
5	/CSL	L	Leftchip select signal input terminal.
6	/CSR	H/L	rightchip select signal input terminal.
7	CL	—	Display data latch signal input terminal.
8	E(/RD)	H/L	Chip enable signal
9	R/W(/WR)	H/L	Read/Write control signal input terminal.
10~17	D0~D7	H/L	Tri-state bilateral Data Bus.
18	/RST	H/L	Reset and interface type select terminal.
19	LEDA	—	LED backlight power supply (+)
20	LEDK	—	LED backlight power supply (-)